

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re application of: | |) | |
| | Nobuhiro Nunoya et al. |) | |
| Serial No.: | 10/527,355 |) | Art Unit |
| Filing Date: | March 7, 2005 |) | 2828 |
| Confirmation No.: | 4938 |) | |
| For: | OPTICAL SEMICONDUCTOR DEVICE AND OPTICAL SEMICONDUCTOR INTEGRATED CIRCUIT |) | |
| Examiner: | Patrick Stafford |) | |

AMENDMENT "B" AND RESPONSE TO FINAL OFFICE ACTION

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Examiner:

Applicant submits this paper in response to the Final Office Action dated June 28, 2007.
Reconsideration is respectfully requested in view of the following remarks:

A Listing of the Claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.